



Out Reference: ESI-116-B

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Patrick F. Leonard  
Serial No. 09/680,342  
Filing Date: October 5, 2000  
Examiner/Art Unit C. Kao  
Title: METHOD AND APPARATUS FOR EVALUATING  
INTEGRATED CIRCUIT PACKAGES HAVING THREE  
DIMENSIONAL FEATURES

NOTICE OF APPEAL

Commissioner for Patents  
P O Box 1450  
Alexandria VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from  
the decision dated March 29, 2003 of the Examiner's final rejection of claims 17-41. *W. B. B.*

Applicant's attorney's check in the amount of Three Hundred Twenty (\$320.00)

Dollars is enclosed to cover the cost of the Notice of Appeal filing fee.

Respectfully submitted,

YOUNG & BASILE, P.C.

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Dated: July 24, 2003  
TEB/sld

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